

Substitute for Form 1449A/PTO (Modified)		Complete if Known		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Application Number	Unassigned	
		Filing Date	Herewith	
		First Named Inventor:	Tsailai Terry Wu	
		Group Art Unit	Unassigned	
		Examiner Name	Unassigned	
Sheet		of	Attorney Docket Number	3935P004C2

OTHER ART - NO PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
1h		WATANABE ET AL, "A Modular Architecture for a 6.4 Gbyte/s, 8-Mb DRAM Integrated Media Chip", IEEE Journal of Solid State Circuits, Vol. 32, No. 5, May 1997, pages 635-641.	
1h		JUNKO YOSHIDA, "Startup Turns Up Heat in 3-D Graphics Chips", August 17, 1998, Electronic Engineering Times, page 26.	

Examiner Signature	<i>K. Terry</i>	Date Considered	<i>9/20/04</i>
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

¹Unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.

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